

Wavelet Packet Decomposition-Based Fault Diagnosis Scheme for SRM Drives With a Single Current Sensor

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Abstract—Power converters are a key, but vulnerable component in switched reluctance motor (SRM) drives. In this paper, a new fault diagnosis scheme for SRM converters is proposed based on the wavelet packet decomposition (WPD) with a dc-link current sensor. Open- and short-circuit faults of the power switches in an asymmetrical half-bridge converter are analyzed in details. In order to obtain the fault signature from the phase currents, two pulse-width modulation signals with phase shift are injected into the lower-switches of the converter to extract the excitation current, and the WPD algorithm is then applied to the detected currents for fault diagnosis. Moreover, a discrete degree of the wavelet packet node energy is chosen as the fault coefficient. The converter faults can be diagnosed and located directly by determining the changes in the discrete degree from the detected currents. The proposed scheme requires only one current sensor in the dc link, while conventional methods need one sensor for each phase or additional detection circuits. The experimental results on a 750-W three-phase SRM are presented to confirm the effectiveness of the proposed fault diagnosis scheme.

Index Terms—Current sensor, discrete degree, fault diagnosis, node energy, power converter, pulse-width modulation (PWM), switched reluctance motor (SRM), wavelet packet decomposition (WPD).

I. INTRODUCTION

IN RECENT years, switched reluctance motors (SRMs) have attracted much attention from industry and research community, primarily due to their rare-earth-free feature, while competing permanent magnet machines rely on rare-earth materials (e.g., neodymium and dysprosium). The SRM has a simple and rugged rotor structure, as well as advantages of high efficiency, low cost, and high reliability [1]–[4]. It is particularly suited for high-speed and safety-critical applications [5], [6]. The SRM is

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also considered as a competitive solution for electric propulsion in electric vehicles and hybrid electric vehicles [4], [7]–[10].

It is known that SRM drives are fault-tolerant by their nature [11]–[13], but not completely fault-free. For instance, power converters are a key component in motor drives and their power switches are the most susceptible to failures, especially in harsh environments. In practice, open- and short-circuit faults of the power switches are the common failures [14]–[16], which lead to an adverse impact on the system performance. Although the SRM can still operate if a phase leg is lost to failure, its electromagnetic and mechanical performance deteriorates to some degree and the rotor is subjected to unbalanced force.

Some state-of-the-art technologies are proposed in [17]–[21], to identify the open- and short-circuit faults for brushless dc (BLDC) motor, permanent-magnet synchronous motor (PMSM), and induction motor (IM) drives. In [17], the wavelet transform is used to extract the diagnostic indices from the dc-link current waveform under open-switch conditions for BLDC motor drives. An online fault diagnosis method for BLDC inverter with a dc–dc buck converter is proposed in [18]. Based on the residual signals of the voltage observers, the method can detect both open- and short-circuit faults in power switches. In [19], a model reference adaptive system is developed to detect open-circuit faults in power switches of PMSM inverters. Another diagnostic method for open-circuit faults in the closed-loop controlled PMSM drives is presented in [20], based on the current residual vector. In [21], a simple diagnostic method for single-switch and double-switch open-circuit faults is developed for pulse-width modulation (PWM) voltage-source inverters in vector controlled IM drives.

For SRMs, electrical faults are classified in details in [22], and several remedial strategies are also presented. The artificial neural networks (ANNs) and evolutionary algorithms-based model [23], and the ANNs and genetic algorithms-based model [24] are employed to predict the performance of SRM drives under normal and faulty operating conditions. In [25], two schemes by monitoring the chopped bus current and by detecting the upper freewheeling bus current and excitation bus current are proposed and compared in detail for power switch faults. The work in [26] presents a fault diagnostic technique for open- and short-circuit faults in SRM power switches, based on the difference between the measured and estimated dc-link currents. However, one extra current sensor is also needed in addition to the phase current sensors, increasing the cost and volume of the drive. In [27], a similar diagnostic method is proposed based on the difference

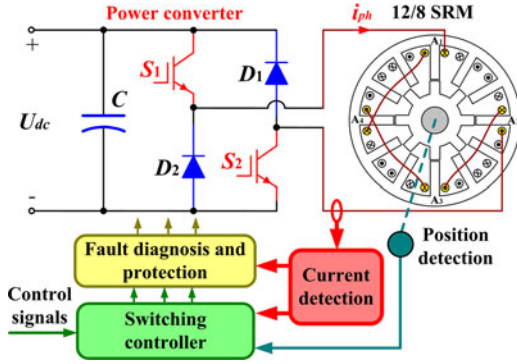


Fig. 1. 12/8-pole SRM drive system for one phase leg.

between the averages of the phase currents. Moreover, the supply current polar chart is used for converter faults analysis [28]. Other techniques such as current differential detectors and flux differential detectors [29] are found in SRM drives for faults detection. A fault diagnosis scheme by using voltage dividers and comparators to monitor the voltage of lower-switches is presented in [30], for an inverter-fed SRM drive. However, they need additional detection circuits and inevitably increase the cost and complexity to the motor drive. Fast Fourier transform (FFT) algorithm is employed in [31] and [32] to analyze the supply current of the power converter subjected to open- and short-circuit faults. Nevertheless, the effective fault coefficient is not presented, and the diagnosis accuracy is easily affected by the spectrum energy leakage from the FFT algorithm. As a result, there is a need to develop an effective, reliable and low-cost diagnostic method for SRM converter faults.

In general, effective fault diagnosis requires accurate current tracking in a motor drive. Conventionally, a current sensor is installed in each phase to track the phase current. In order to reduce the cost and volume of the motor drive, some technologies [33]–[37] use a single current sensor to detect the phase current from the dc-link current. A tristate PWM technique is employed in [33] to reconstruct the phase current of the IM drive using dc current information. In [34], a new phase current reconstruction method is proposed for PMSM servo inverters using a single dc-link current sensor by applying phase-shifts to the switching-state waveforms. This leads to lower output current ripples and switching losses. A new algorithm using a single dc-link current sensor to reconstruct all three phase currents of the inverters is proposed in [35], by applying additional active voltage vectors for brief intervals during each fundamental frequency cycle. A pulse injection method of phase current reconstruction from dc-link current is presented in [36] for SRM converters. A six-phase current reconstruction scheme with a single dc-link current sensor for dual traction inverters is proposed in [37], by using advanced phase shift for all PWM signals based on the sequence of duty cycles.

This paper proposes a new fault diagnosis scheme for SRM drives based on the wavelet packet decomposition (WPD) algorithm and a single dc-link current sensor. It employs the discrete WPD because of its good frequency resolution and localization.

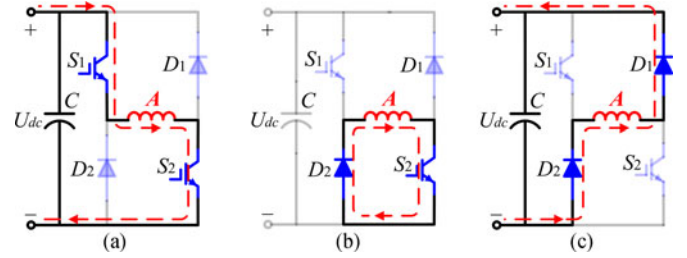


Fig. 2. Operation modes of the converter. (a) Excitation mode. (b) Freewheeling mode (ZVL). (c) Demagnetization mode.

Open- and short-circuit faults in power switches in an asymmetrical half-bridge converter are analyzed in details. Two phase-shifted PWM signals with the same frequency and duty-cycle are injected simultaneously into the lower-switch of each phase for excitation current detection. Furthermore, a discrete degree of the wavelet packet node energy is used as the fault coefficient to diagnose and locate the faults. The proposed scheme requires only one current sensor in the dc-link without any hardware investment. The experiments carried out on a three-phase 12/8-pole SRM are presented to confirm the effectiveness and implementation of the proposed fault diagnosis scheme.

II. ANALYSIS OF FAULTS IN POWER SWITCHES

A. SRM Drive

Fig. 1 shows a 12/8-pole SRM drive system for one phase leg. The current detection is very important to diagnose the fault that occurs in the drive to implement protections. A conventional asymmetrical half-bridge converter is usually employed in the SRM drive, due to its phase isolation, excellent stability and fault tolerance performance. The power switches in the drive are a vulnerable part, most prone to failures.

In order to reduce the switching power loss, a soft-chopping mode is used such that the upper-switch chops and the lower-switch remains closed in the phase turn-on region [25]. There are mainly three operation modes for the converter, as shown in Fig. 2. When switches S_1 and S_2 are both ON in the phase turn-on region, the current flows in phase A winding immediately, and the phase leg works in the excitation mode, as shown in Fig. 2(a). When S_1 is OFF and S_2 is ON, the phase A current is in a zero-voltage loop (ZVL), and the phase leg works in the freewheeling mode, as shown in Fig. 2(b). When S_1 and S_2 both shut OFF in the phase turn-off region, the phase current flows back to the power source through diodes D_1 and D_2 , and the phase leg works in the demagnetization mode, as shown in Fig. 2(c).

A speed-controlled closed-loop SRM drive with voltage-PWM regulation scheme is shown in Fig. 3. The speed error is processed by a speed controller, such as a proportional integral (PI) controller. The motor speed is obtained from a speed calculator using an encoder to detect the rotor position. The turn-on and turn-off angles (θ_{on} and θ_{off}) are determined by the position detector to control the phase commutation. The phase voltage is the control issue, which is addressed by the

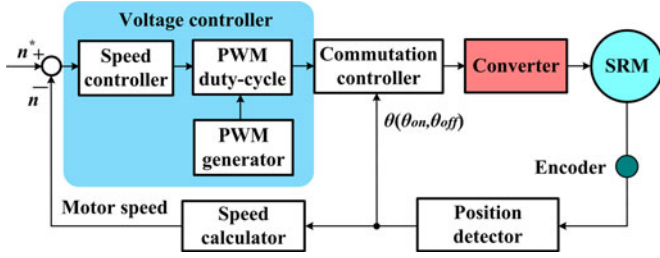
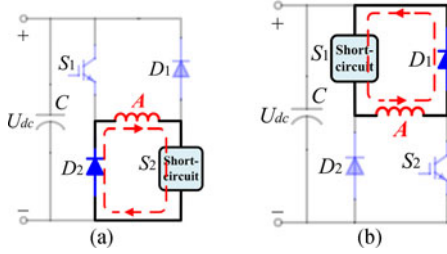


Fig. 3. Control diagram for SRM drives.

Fig. 4. Short-circuit fault condition. (a) S_2 short-circuit. (b) S_1 short-circuit.

voltage controller. The PWM duty-cycle is regulated by a PWM generator according to the instantaneous speed.

B. Open-Circuit Fault

If there is an open-circuit fault developed in the upper-switches or lower-switches, the system will work in the phase absence state. The average electromagnetic torque under healthy conditions is given by

$$T_{av} = \frac{mN_r}{2\pi} \int_0^{2\pi/N_r} T_e(\theta, i) d\theta \quad (1)$$

where m is the phase number, N_r is the number of rotor poles, θ is the rotor angular position, i is the phase current, and $T_e(\theta, i)$ is the phase instantaneous torque depending on the rotor position and phase current.

When one phase leg is open-circuited, the faulty phase loses excitation and other phases can still operate normally, due to the phase isolation of the converter. The average electromagnetic torque becomes

$$T_{fav} = \frac{m-1}{m} T_{av}. \quad (2)$$

In a closed-loop system, the currents in the healthy phases will increase to produce the required torque output, due to the adjustment of the speed controller. However, the unbalanced phase currents increase the torque ripple and reduce the load capacity.

C. Short-Circuit Fault

When the lower-switch S_2 is short-circuited, the only free-wheeling mode is illustrated in Fig. 4(a); when the short-circuit fault of upper-switch S_1 occurs, the only freewheeling mode

is illustrated in Fig. 4(b). In both faulty conditions, the phase current in the phase turn-off region is in a ZVL.

In healthy conditions, the voltage equation (e.g., phase A) in the phase turn-on region is given by

$$U_a = DU_{dc} = R_a i_a + L_a(\theta_r, i_a) \frac{di_a}{dt} + i_a \omega_r \frac{dL_a(\theta_r, i_a)}{d\theta_r} \quad (3)$$

where D is the PWM duty-cycle, U_{dc} is the dc-link voltage, R_a is the phase A winding resistance, i_a is the phase A current, θ_r is the rotor position, $L_a(\theta_r, i_a)$ is the phase A winding inductance depending on the rotor position and phase current, and ω_r is the rotor angular speed.

When S_2 is short-circuited, the phase A voltage still can be modulated normally by the PWM signal in S_1 , and phase A leg can still be controlled freely in the phase turn-on region. In the phase turn-off region, S_1 is turned OFF and phase A current is in a ZVL through D_2 , as shown in Fig. 4(a). The phase A current cannot flow back to the power source, and the demagnetization current cannot decrease to zero, which enters into the inductance descending region. A negative torque is generated according to

$$T_a = \frac{1}{2} i_a^2 \frac{dL_a(\theta_r, i_a)}{d\theta_r}. \quad (4)$$

In this state, the torque ripple is increased due to the negative torque.

When S_1 is short-circuited, in the phase turn-on region, the PWM-voltage control scheme cannot be implemented by S_1 , the voltage on phase A winding is the dc-link voltage, and the phase currents are more unbalanced compared to S_2 short-circuited state. The voltage equation can be expressed as

$$U_a = U_{dc} = R_a i_a + L_a(\theta_r, i_a) \frac{di_a}{dt} + i_a \omega_r \frac{dL_a(\theta_r, i_a)}{d\theta_r}. \quad (5)$$

Similarly, in the phase turn-off region, S_2 is turned OFF and phase A current is in a ZVL through D_1 , as shown in Fig. 4(b). The phase current cannot flow back to the power source, which makes the current enter into the inductance descending region and increases the torque ripple.

III. PROPOSED FAULT DIAGNOSIS SCHEME

A. Phase Current

Fig. 5 shows the relationship between the phase currents and switching states. In the figure, i_a , i_b , and i_c are the phase A, B, and C currents, respectively; S_2 , S_4 , and S_6 are the drive signals for the lower-switches of phases A, B, and C, respectively; θ_1 , θ_4 , and θ_5 are the turn-on angle, turn-off angle, and current ending angle for phase B, respectively; θ_2 is the turn-off angle of phase A; and θ_3 is the turn-on angle of phase C.

There are four regions in the diagram. Regions I and III are the excitation current overlapping regions. Hence, the dc-link current in this region is the sum of the overlapped excitation currents. In Region II, there are the excitation current of phase B and demagnetization current of phase A in the dc-link current. In Region IV, the dc-link current contains both the demagnetization current of phase B and the excitation current of phase C. If the demagnetization currents are all removed from the dc-link

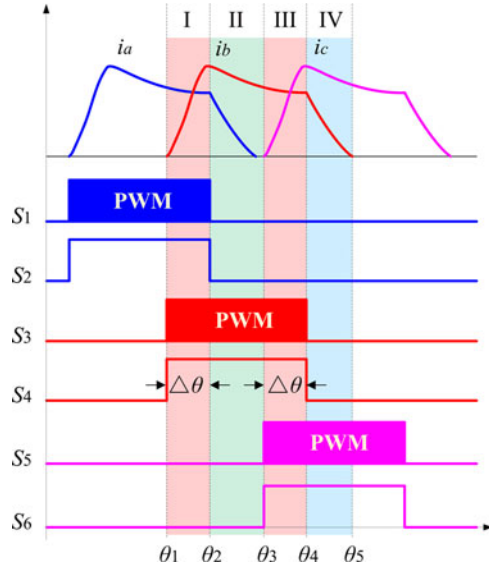


Fig. 5. Relationship between the phase currents and switching states.

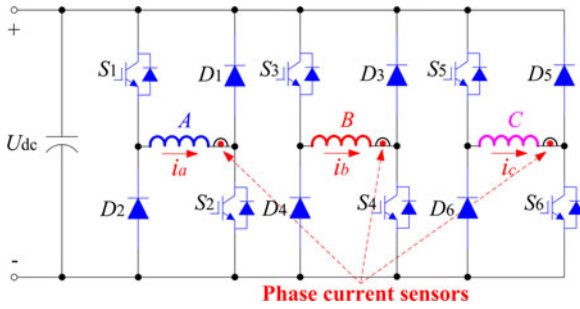


Fig. 6. Conventional current sensor placement strategy.

current, only the excitation current of phase B is present in the dc-link current in Region II, and only the excitation current of phase C is contained in the dc-link current in Region IV. Hence, the dc-link current without the demagnetization current contained in the rotor position region of θ_1 – θ_5 can be expressed as

$$i_{dc} = \begin{cases} i_a + i_b, & \theta_1 < \theta \leq \theta_2 \\ i_b, & \theta_2 < \theta \leq \theta_3 \\ i_b + i_c, & \theta_3 < \theta \leq \theta_4 \\ i_c, & \theta_4 < \theta \leq \theta_5. \end{cases} \quad (6)$$

B. Excitation Current Detection Using a Single DC-Link Current Sensor

Conventionally, a current sensor is used individually in each phase winding to measure every phase current, as shown in Fig. 6. However, the used current sensors inevitably add the cost and volume to the motor drive. In order to obtain a compact and cost-effective system, a single dc-link current sensor strategy is proposed in Fig. 7. The positive terminals of all lower-diodes are connected together to the cathode of the power source, and all the emitters of the lower-switches are also connected

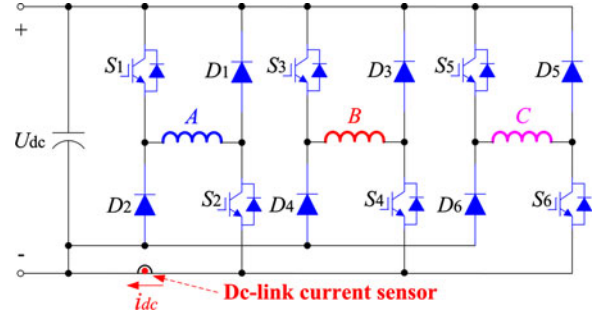


Fig. 7. Proposed dc-link current sensor placement strategy.

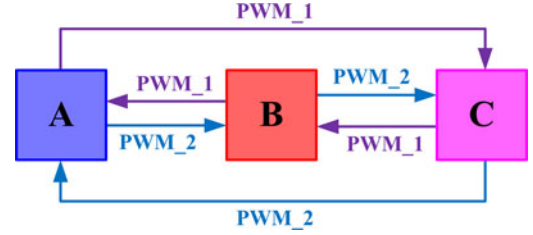


Fig. 8. PWM injection mode.

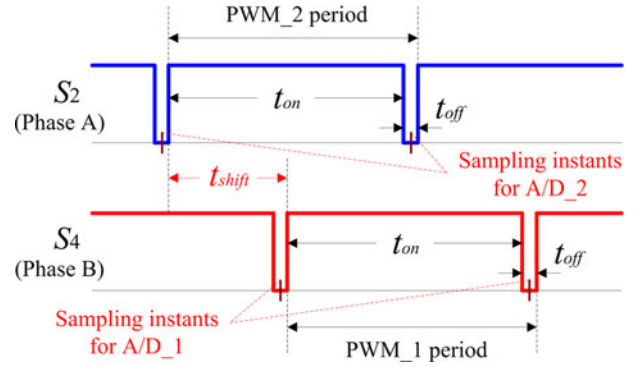


Fig. 9. Sampling instants and phase-shift condition.

together though a dc-link current sensor to the cathode of the power source. The proposed topology only has the excitation and freewheeling modes across the dc-link current sensor, without the demagnetization current of each phase contained in the proposed dc-link current.

The switching functions of the converter are defined as

$$S_{2,4,6} = \begin{cases} 1, & \text{Lower – switch is ON} \\ 0, & \text{Lower – switch is OFF.} \end{cases} \quad (7)$$

According to the phase currents and switching functions, the dc-link current can be expressed as

$$i_{dc} = i_a S_2 + i_b S_4 + i_c S_6. \quad (8)$$

In order to obtain the excitation currents from the dc-link current, a phase-shifted PWM injection method is proposed and the PWM injection mode is shown in Fig. 8. Fig. 9 shows the sampling instants and phase-shift condition, where t_{on} and t_{off} are the turn-on and turn-off times of the PWM in a fundamental frequency cycle, and t_{shift} is the phase-shift time between the two

PWM signals. For example, PWM_1 is injected into the lower-switch of phase B in the excitation current overlapped region of phases A and B (Region I). The lower-switch of phase B is shut OFF in the turn-off time of PWM_1. According to the new converter configuration in Fig. 7, phase B current does not flow in the current sensor, and there is only the excitation current of phase A in the dc-link current during the inserted turn-off time of PWM_1. In this condition, one analog to digital (A/D) converter channel A/D_1 is triggered in the PWM_1 pause middle to detect the excitation current of phase A from the dc-link current. Furthermore, in order to detect the excitation current of phase B in Region I, the lower-switch of phase A should be shut OFF in other staggered turn-off times. Therefore, PWM_2, shifted by t_{shift} from PWM_1, is injected into the lower-switch of phase A in the overlapped region of phases A and B. Similarly, phase A current does not flow in the current sensor, and there is only the excitation current of phase B in the dc-link current during the inserted turn-off time of PWM_2. In this condition, another A/D converter channel A/D_2 is triggered in the PWM_2 pause middle to detect the excitation current of phase B from the dc-link current. Based on this technique, the excitation currents can be separated and detected individually in their overlapped regions. Therefore, the excitation current of each phase in the phase turn-on region can be completely obtained by follows:

$$i_{a_re} = i_{dc_PWM2} \cdot S_2 \cdot S_6 + i_{dc_PWM1} \cdot S_2 \cdot S_4 + i_{dc} \cdot S_2 \cdot \overline{S_2 S_6} \cdot \overline{S_2 S_4} \quad (9)$$

$$i_{b_re} = i_{dc_PWM2} \cdot S_2 \cdot S_4 + i_{dc_PWM1} \cdot S_4 \cdot S_6 + i_{dc} \cdot S_4 \cdot \overline{S_2 S_4} \cdot \overline{S_4 S_6} \quad (10)$$

$$i_{c_re} = i_{dc_PWM2} \cdot S_4 \cdot S_6 + i_{dc_PWM1} \cdot S_2 \cdot S_6 + i_{dc} \cdot S_6 \cdot \overline{S_4 S_6} \cdot \overline{S_2 S_6} \quad (11)$$

where S_2 , S_4 , and S_6 are the drive signals prior to the PWMs injection; i_{a_re} , i_{b_re} , and i_{c_re} are the detected excitation currents for phases A, B, and C, respectively; i_{dc_PWM1} and i_{dc_PWM2} are the measured dc-link currents with PWM_1 and PWM_2 injections in the pause middle of the PWMs in Regions I and III, respectively; and i_{dc} is the sampled dc-link current without any PWM injection in Region II.

As illustrated in Fig. 9, in order to ensure an effective sampling when injecting the PWM signals, the phase-shift time t_{shift} should be limited to avoid the simultaneous turn-off of two phases. Therefore

$$t_{\text{off}} < t_{\text{shift}} < t_{\text{on}}. \quad (12)$$

The frequency and duty-cycle of the injected PWM should be set both large enough to ensure a high sampling precision for the excitation current detection and small impact on the actual phase current. However, considering the sensing tolerance of the current sensors, the maximum duty-cycle should be limited to allow a sufficient measurement time [38].

C. Discrete Degree of the Wavelet Packet Node Energy

In this paper, the excitation current detection scheme with the WPD algorithm is used for fault diagnosis. Based on this,

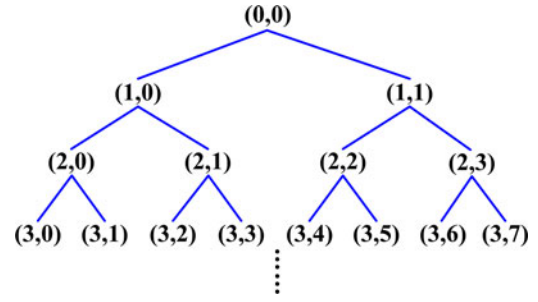


Fig. 10. Illustrative diagram of the WPD.

an effective fault coefficient is proposed. Owing to good frequency resolution and localization [39], the WPD algorithm is applied to the detected excitation current within a current period to extract the fault coefficient. WPD is an extension of the wavelet transform by combining multiresolution approximation with wavelets. The algorithm is to decompose one node into two nodes step by step, i.e., subdividing the whole frequency band of the sampled signal into small segments, as shown in Fig. 10. Obviously, if the decomposition level is l , the number of the nodes after decomposition is 2^l .

The wavelet packet algorithm includes the decomposition and reconstruction for the wavelet packet coefficients. The decomposition algorithm for the coefficients is obtained by

$$\begin{cases} d_k^{j+1,2n} = \sum_k h_{0(2l-k)} d_l^{j,n} \\ d_k^{j+1,2n+1} = \sum_k h_{1(2l-k)} d_l^{j,n} \end{cases} \quad (13)$$

where $d_l^{j,n}$, $d_k^{j+1,2n}$, and $d_k^{j+1,2n+1}$ are all the wavelet packet coefficients, and $h_{0(2l-k)}$ and $h_{1(2l-k)}$ are the low-pass and high-pass filter coefficients for decomposition, respectively.

The reconstruction algorithm for the wavelet packet coefficients is deduced as

$$d_l^{j,n} = \sum_k g_{0(l-2k)} d_k^{j+1,2n} + \sum_k g_{1(l-2k)} d_k^{j+1,2n+1} \quad (14)$$

where $g_{0(l-2k)}$ and $g_{1(l-2k)}$ are the low-pass and high-pass filter coefficients for reconstruction, respectively.

For instance, if the decomposition level is five, the number of the obtained nodes is 32 after the wavelet packet algorithm calculations. Hence, the frequency band of the sampling signal is subdivided into 32 small segments. The wavelet packet coefficient of each node on the fifth level can be calculated from (13) and (14). The energy of node j on the fifth level is given by

$$E(5, j) = \int |S(5, j)(t)| dt = \sum_{k=1}^n |d_{j,k}|^2 \quad (15)$$

where $d_{j,k}$ ($j = 0, 1, \dots, 31$; $k = 1, 2, \dots, n$) is the wavelet packet coefficient for the reconstructed signal $S(5, j)$.

Hence, the discrete degree of the node energy can be expressed as

$$\sigma = \left\{ \frac{1}{k} \sum_{n=1}^k [E(5, n) - E_{av}]^2 \right\}^{\frac{1}{2}} \quad (16)$$

TABLE I
PROPOSED FAULT DIAGNOSIS TECHNIQUE COMPARING WITH EXISTING FAULT DIAGNOSIS METHODS

	Proposed method	Paper [23], [24]	Paper [25]	Paper [26]	Paper [27]	Paper [29]
Current sensor	One	One for each phase	Two	One for each phase + 1	One for each phase	One for each phase
Fault diagnosis	Easy	Complicated	Complicated	Easy	Complicated	Easy
Fault tolerance	Yes	Yes	Yes	Yes	Yes	Yes
Robustness	High	Low	Low	High	Low	High
Circuitry change	Minor	No	Medium	No	No	No
Cost	Low	High	Medium	High	High	High

where E_{av} is denoted as

$$E_{av} = \frac{1}{k} \sum_{n=1}^k E(5, n) \quad (17)$$

where $E(5, n)$ is the energy of node n on the fifth level, k is the selected number of the nodes, and E_{av} is the average of the nodes energy.

The discrete degree of the wavelet packet node energy is calculated from the detected excitation current within a current period, and it is defined as the fault coefficient for fault diagnosis and location of the faulty switch.

D. Comparison of the Proposed Fault Diagnosis Scheme with Existing Strategies

A detailed comparison of the proposed fault diagnosis technique with existing fault diagnosis methods is presented in Table I. The ANN algorithm is employed in [23] and [24] for prediction of motor performance under faulty conditions, which is relatively difficult and complex, and the variations of the control parameters are not considered in this ANN-based model. An offline table of the current states under different faults is presented in [25] by detecting the upper freewheeling bus current and excitation bus current, and the converter circuit is modified for the current sensors installation. However, it is not suited for online angle modulation and variable load systems. In [26], one extra current sensor is needed in addition to the phase current sensors to measure the dc-link current for comparison with the estimated dc-link current calculated from the phase currents, which increase the cost and complexity of the drive. In [27], the difference between the averages of the phase currents is calculated to diagnose the faults for SRM drives with four current sensors used for four phases. The diagnostic system under different control parameters is not investigated. Additional detection devices including current differential detector and flux differential detector are utilized in [29] for faults detection, which increase the cost and complexity to the motor drive.

Compared to the existing schemes in [23]–[27], and [29], the proposed fault diagnosis scheme uses only a dc-link current sensor without any additional diagnosis devices and much change to the circuitry, which will considerably reduce the volume and complexity of the motor drive. The proposed scheme is found to be more accurate and easier to implement for fault diagnosis purposes by extracting the fault coefficient from the detected current through WPD algorithm. The cost is determined by the number of current sensors and additional detection circuits

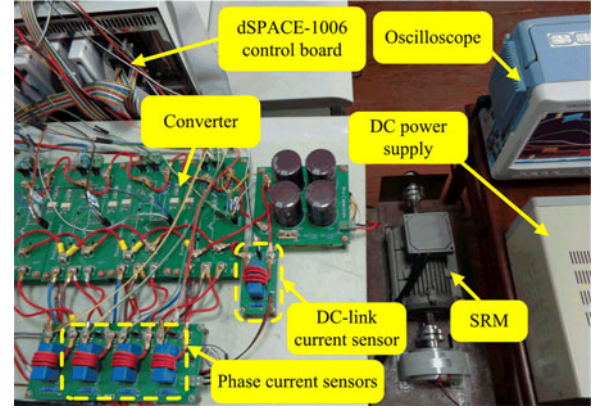


Fig. 11. Experimental setup.

employed in the diagnosis system. The proposed fault diagnosis scheme offers a low-cost solution to typical power switch faults. It has excellent robustness to the variations of system parameters, including the speed regulation, angle modulation, and load variation, which will be proved in the next section.

IV. EXPERIMENTAL VERIFICATION

The proposed fault diagnosis method is experimentally validated on a 750-W three-phase 12/8-pole prototype SRM. The experimental setup is shown in Fig. 11. A dSPACE-DS1006 control board is used as the main controller to implement the proposed scheme. An adjustable dc power supply is employed in the system to drive an asymmetrical half-bridge converter with a 60-V voltage. The dc-link current is detected by a Hall-effect current sensor (LA-55P), and simultaneously sampled by a 14-bit A/D converter. For comparison, three additional current sensors are installed in each phase leg to measure the phase currents. The power switches are IGBTs IKW75N60T and the diodes are IDW75E60.

Fig. 12 shows the block diagram of the implemented SRM drive control strategy. Two PWM signals with phase shift are injected into the lower-switches to generate the new drive signals for the converter, and the phase currents are detected from the sampled dc-link current through the operational amplifiers. The injected PWM signals are running with 10-kHz switching frequency, 95% duty-cycle, and 50- μ s phase-shift time. The rotor position and motor speed are obtained from an incremental encoder. A PI algorithm is employed in the system to regulate the motor speed via the closed-loop control.

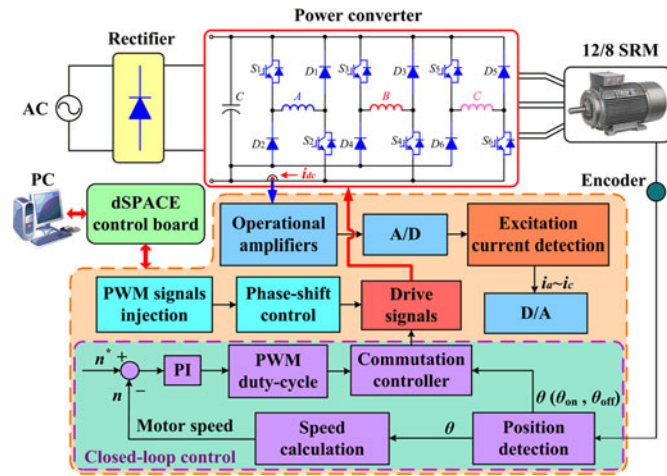


Fig. 12. Block diagram of the implemented SRM drive control strategy.

A. Excitation Current Detection

Fig. 13 shows the phase current detection in healthy conditions at 600 r/min and 1-N.m load. Clearly, in Fig. 13(a), the dc-link current is the sum of the excitation currents. In order to detect the phase B current, PWM_2 is injected into the lower-switch of phase A in the overlapped region of phases A and B, and A/D_2 is triggered in the PWM_2 pause middle to sample the excitation current of phase B in this region; and PWM_1 is injected into the lower-switch of phase C in the overlapped region of phases B and C, and A/D_1 is triggered in the PWM_1 pause middle to sample the excitation current of phase B in this region, as shown in Fig. 13(b). PWM_2 and PWM_1 are injected simultaneously into the lower-switches of phases B and C in their overlapped regions, and A/D_2 and A/D_1 are triggered in PWM_2 and PWM_1 pause middle, respectively, to separate the overlapped excitation currents, as shown in Fig. 13(c). Clearly, due to a large duty-cycle and a high frequency, the turn-off time of the injected PWM is extremely short, which has little impact on the detected current.

In a short-circuit fault in the lower-switch S_4 , the demagnetization current of phase B cannot deplete in a ZVL and the excitation current increases obviously, as shown in Fig. 14(a). However, the voltage can also be regulated in the phase turn-on region and the actual phase A and C currents are not affected by the fault in phase B. Fig. 14(b) shows the phase B current detection by employing the PWM injection scheme when S_4 is short-circuited. The demagnetization current of phase B is always present in the dc-link current, and the detected phase A and C currents under PWM injection will both contain the demagnetization current of phase B. If the short-circuit fault occurs in the upper-switch S_3 , the voltage cannot be regulated in the phase turn-on region, and the demagnetization current of phase B is also in a ZVL in the phase turn-off region, which is similar to the short-circuit fault in the lower-switch, as shown in Fig. 14(c). Clearly, the detected phase A and C currents are not affected by the fault, and can, thus, be used to distinguish the short-circuit fault from the upper-switch or the lower-switch. Fig. 14(d) shows the phase B current detection under PWM injection when S_3 is short-circuited.

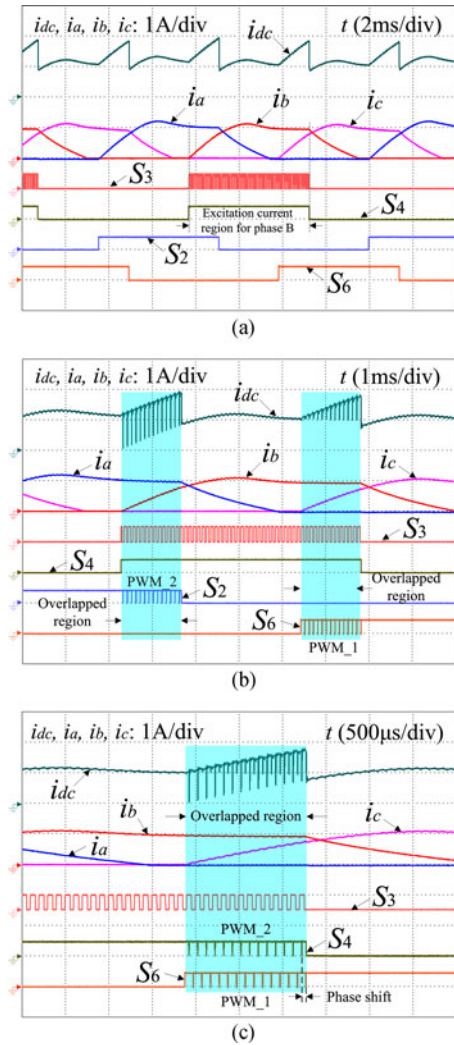


Fig. 13. Phase current detection in healthy condition under PWM injection. (a) Healthy condition without PWM injection. (b) Phase B current detection. (c) Current separation for phases B and C from dc-link current.

The motor drive will work in the absence of one phase leg, no matter the open-circuit fault occurs in the upper-switch or in the lower-switch. In Fig. 15(a), the phase B current is disappeared when the upper-switch S_3 is open-circuited. Hence, there is no detected current in phase B under PWM injection, as shown in Fig. 15(b). The dc-link current is only phase A current in the overlapped region of phases A and B, and only phase C current in the overlapped region of phases B and C.

Fig. 16 shows the dynamic speed response and the transient progression of the closed-loop system in faulty conditions. The motor can remain operational due to the phase isolation and fault tolerance characteristics of the SRM drive. Phase A and C currents are not affected by the open- and short-circuit faults in the phase B leg. As illustrated in the figure, the motor speed is stabilized within two current fundamental periods and follows the given values well regardless of the open- and short-circuit faults. This is due to the PI algorithm, which ensures a fast and accurate detection for the steady-state currents.

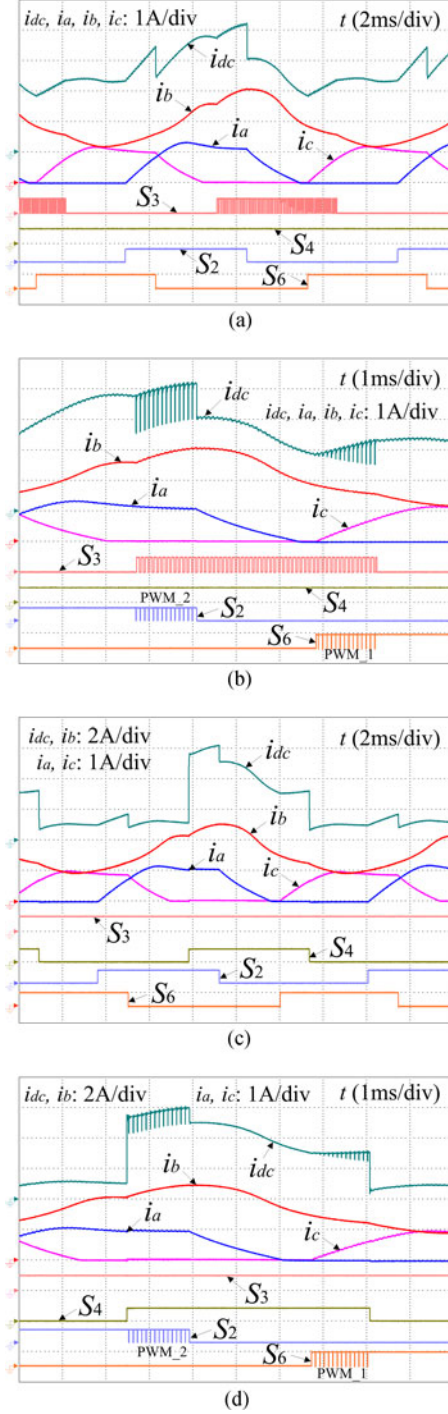


Fig. 14. Phase current detection for the short-circuit fault. (a) Short-circuit fault in the lower-switch. (b) Phase B current detection when the lower-switch is short-circuited. (c) Short-circuit fault in the upper-switch. (d) Phase B current detection when the upper-switch is short-circuited.

B. Extraction of Fault Coefficient

The WPD algorithm is used to find the fault coefficient. The orthogonal mother wavelet “db3” are selected to implement the WPD algorithm in the sampled currents. In order to make an accurate division of the frequency band to ensure the analysis precision, the five-level decomposition is selected. Considering

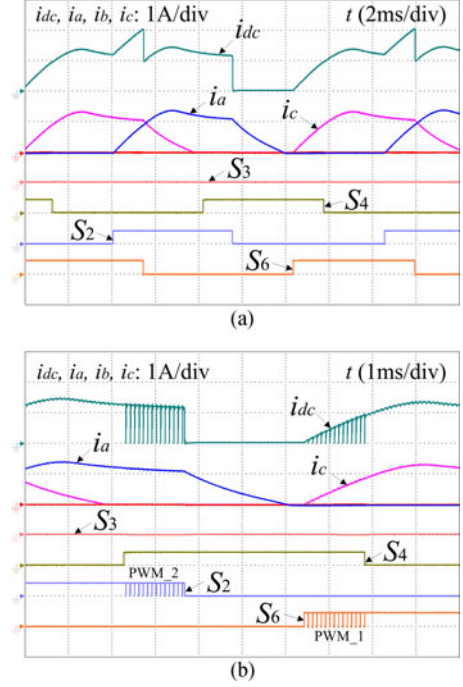


Fig. 15. Phase current detection for the open-circuit fault. (a) Open-circuit fault in the upper-switch. (b) Phase B current detection when the upper-switch is open-circuited.

that the nodes after five-level decomposition are too many and the nodes energy is more concentrated on the low-frequency band, the first eight nodes are used to extract the fault features to reduce the computational complexity. Table II shows the frequency bandwidth and corresponding nodes for the first eight nodes, where f is the Nyquist frequency, i.e., half of the current sampling frequency.

Fig. 17 presents the discrete degree of the wavelet packet node energy calculated from the detected excitation currents before and after the short-circuit faults at different speeds. In Fig. 17(a), curve a is the discrete degree of phase B current under healthy conditions; and curves b , c , and d are the discrete degrees of phase B, A, and C currents, respectively, when the upper-switch S_3 is short-circuited. In normal operations, the discrete degree decreases when the motor speed is up to 800 r/min and is stabilized around 18 above 800 r/min. However, the value is obviously increased by 32% at 200 r/min and 66.7% at 1500 r/min, when a short-circuit fault occurs in the upper-switch, while the discrete degrees of phase A and C currents both remain in the normal state, which can be used for the fault diagnosis.

In Fig. 17(b), curve a is the discrete degree of phase B current under healthy conditions; and curves e , f , and g are the discrete degrees of phase B, A, and C currents, respectively, when the lower-switch S_4 is short-circuited. In this faulty condition, the discrete degree of phase B current also increases and the value is similar to the upper-switch short-circuited condition. Nonetheless, the discrete degrees of the detected phase A and C currents both increase, due to the demagnetization current of phase B in the lower ZVL, which is used to tell whether the short-circuit fault occurs in the upper-switch or the lower-switch.

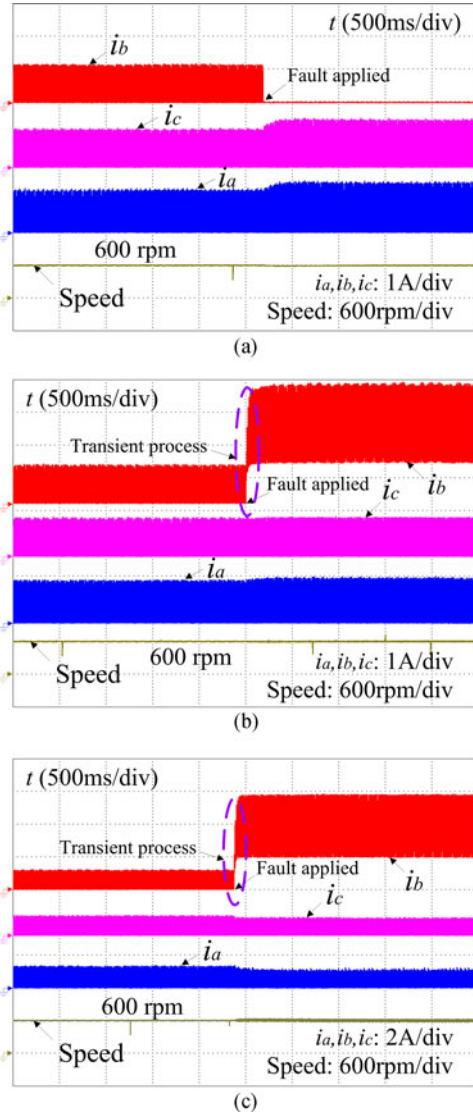


Fig. 16. Transient response of the closed-loop system in faulty conditions. (a) Open-circuit fault in the lower-switch. (b) Short-circuit fault in the lower-switch. (c) Short-circuit fault in the upper-switch.

TABLE II
FREQUENCY BANDWIDTH AND CORRESPONDING NODES

Bandwidth	Node	Bandwidth	Node
(0, $f/32$)	(5,0)	($f/8$, $5f/32$)	(5,6)
($f/32$, $f/16$)	(5,1)	($5f/32$, $3f/16$)	(5,7)
($f/16$, $3f/32$)	(5,3)	($3f/16$, $7f/32$)	(5,5)
($3f/32$, $f/8$)	(5,2)	($7f/32$, $f/4$)	(5,4)

Phase B current is disappeared when the upper-switch S_3 is open-circuited; hence, the discrete degree of the nodes energy will sharply drop to zero when the fault happens. Thus, the open-circuit fault can be just diagnosed by checking if the fault coefficient changes to zero. However, it should be noted that the asymmetrical half-bridge converter employs two power switches in each phase with phase isolation and fault tolerance

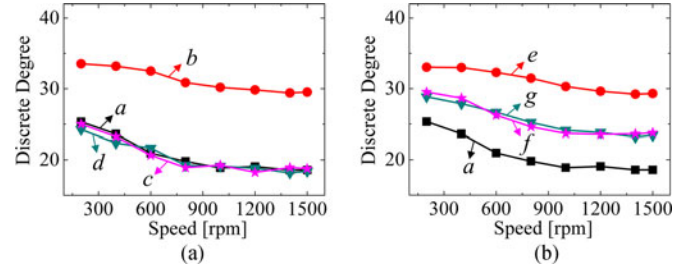


Fig. 17. Discrete degree before and after the faults. (a) Upper-switch short-circuit. (b) Lower-switch short-circuit.

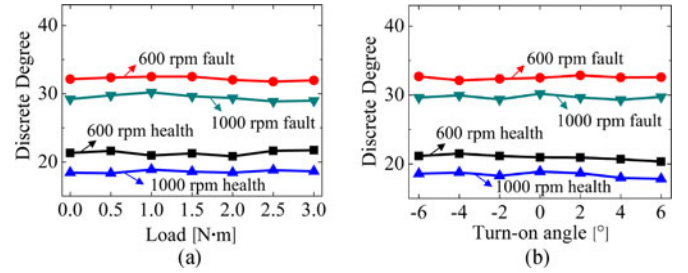


Fig. 18. Impact of the load and turn-on angle on the discrete degree of short-circuit fault in the upper-switch. (a) Discrete degree under different loads. (b) Discrete degree under different turn-on angles.

features. The two switches and phase winding are connected in series. The current can be supplied to the phase winding only when the two switches are both turned ON. No matter an open-circuit fault occurs in the upper-switch or the lower-switch, the affected phase will be in the phase absent state, presenting the same faulty condition. Therefore, in the case of open-circuit faults, due to the specific features of the power converter topology, there could not be able to locate the faulty IGBT in upper-switch or lower-switch if the fault occurs.

Additionally, in order to study the robustness of the fault coefficient to the variations of system parameters, the investigations for the discrete degree of the wavelet packet node energy, calculated from the detected excitation currents before and after the short-circuit fault in the upper-switch, under different loads and turn-on angles are presented in Fig. 18. The fault coefficient has good robustness both to the load and turn-on angle variations. Therefore, the proposed scheme is particularly suitable for low cost, variable load operation, and variable angle modulation systems. By determining the changes in the proposed discrete degree of the wavelet packet node energy calculated from the detected excitation current, open- and short-circuit faults of power switches can be accurately identified.

V. CONCLUSION

This paper has presented a new fault diagnosis scheme for SRM drives using a WPD algorithm and a single dc-link current sensor. This reduces the number of current sensors to one for effective fault diagnosis. Open- and short-circuit faults of power switches in a conventional asymmetrical half-bridge converter can be accurately identified. Two PWM signals with the same frequency and duty-cycle under phase-shift modulation

are simultaneously injected into the lower-switches to obtain the phase current characteristics from the dc-link current, and a discrete degree of the wavelet packet node energy is presented to be the fault coefficient for fault diagnosis. Compared with existing methods, the proposed scheme requires only one current sensor in the dc link without any additional hardware investment and much change to the circuitry. The new current sensor placement strategy offers a low-cost solution to typical power switch faults. The proposed scheme is found to be accurate and easy to implement for fault diagnosis purposes. The responses of the extracted fault coefficient to variations of the system parameters including speed, load and turn-on angle modulations are proved to be excellent from testing a 750-W three-phase SRM.

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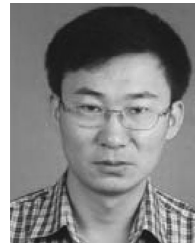
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